

Listing of Claims:

1. (previously presented) A circuit operative to acquire a more-preferred stored SID element comprising:

memory containing a roaming list that includes a plurality of stored SID elements ranked according to an order of preference including at least one more-preferred stored SID element and at least one less-preferred stored SID element; and

logic circuitry, operatively coupled to the memory, and operative to perform a first more-preferred SID acquisition sequence and then a second more-preferred SID acquisition sequence that includes repeatedly attempting acquisition of the at least one more-preferred stored SID element using a same frequency during the second more-preferred SID acquisition sequence.

2. (original) The circuit of claim 1 wherein the logic circuitry is operative to attempt acquisition of the at least one less-preferred stored SID element as part of performing the second more-preferred SID acquisition sequence.

3. (original) The circuit of claim 1 wherein the logic circuitry is operative to perform the second more-preferred SID acquisition sequence if the more-preferred stored SID element is not acquired during the first more-preferred SID acquisition sequence.

4. (previously presented) The circuit of claim 1 wherein the logic circuitry is operative to attempt acquisition by comparing received broadcast SID information with one of the plurality of stored SID elements ranked according to an order of preference including at least one more-preferred stored SID element and at least one less-preferred stored SID element.

5. (previously presented) The circuit of claim 1 wherein the roaming list includes a first more-preferred stored SID element, a second more-preferred stored SID element, and a plurality of less preferred SID elements wherein the logic circuitry is operative to perform the second more-preferred SID acquisition sequence that includes repeatedly attempting acquisition of the first more-preferred stored SID element, and repeatedly attempting acquisition of the second more-preferred stored SID element using the same frequency and a single acquisition attempt of each of the at least one less-preferred stored SID element as part of the second more preferred SID acquisition sequence.

6. (previously presented) A wireless device comprising:

memory containing a roaming list that includes a plurality of stored SID elements ranked according to an order of preference including at least one more-preferred stored SID element and at least one less-preferred stored SID element;

a wireless receiver operative to receive transmitted SID information; and

logic circuitry, operatively coupled to the memory, and operative to perform a first more-preferred SID acquisition sequence and then a second more-preferred SID acquisition sequence that includes repeatedly attempting acquisition of the at least one more-preferred stored SID element using a same frequency during the second more-preferred SID acquisition sequence.

7. (original) The wireless device of claim 6 wherein the logic circuitry is operative to attempt acquisition of the at least one less-preferred stored SID element as part of performing the second more-preferred SID acquisition sequence.

8. (previously presented) A wireless device comprising:

memory containing a roaming list that includes a plurality of stored SID elements ranked according to an order of preference including at least one more-preferred stored SID element and a plurality of less-preferred stored SID elements;

a wireless receiver operative to receive broadcast SID information; and

logic circuitry, operatively coupled to the memory, and operative to perform a first more-preferred SID acquisition sequence and then a second more-preferred SID acquisition sequence that includes, during the second more-preferred SID acquisition sequence, repeatedly attempting acquisition of the at least one more-preferred stored SID element at a same frequency and a single acquisition attempt of each of the plurality of less-preferred stored SID elements not acquired during the first more-preferred SID acquisition sequence as part of the second more preferred SID acquisition,

wherein attempting acquisition is based on a comparison of the received broadcast SID information with one of the plurality of stored SID elements, and

wherein the second more-preferred SID acquisition sequence is again performed if acquisition of the plurality of stored SID elements in the roaming list is unavailable.

9. (original) The wireless device of claim 8 wherein the logic circuitry camps on at least one less-preferred stored SID element if acquisition on the at least one less-preferred stored SID element is available and if acquisition on the at least one more-preferred stored SID element is unavailable, and wherein the logic circuitry camps on the at least one more-preferred SID stored element if the at least one more-preferred stored SID element is acquired at any time.

10. (previously presented) A memory containing computer readable instructions executable by one or more processing devices that causes the one or more processing devices to:

store a roaming list that includes a plurality of stored SID elements ranked according to an order of preference including at least one more-preferred stored SID element and at least one less-preferred stored SID element; and

perform a first more-preferred SID acquisition sequence and then a second more-preferred SID acquisition sequence that includes repeatedly attempting acquisition of the at least one more-preferred stored SID element during the second more-preferred SID acquisition sequence using a same frequency.

11. (original) The memory of claim 10 containing executable instructions that cause the one or more processing devices to attempt acquisition of the at least one less-preferred stored SID element while performing the second more-preferred SID acquisition sequence.

12. (original) The memory of claim 10 containing executable instructions that cause the one or more processing devices to perform the second more-preferred SID acquisition sequence if the more-preferred stored SID element is not acquired during the first more-preferred SID acquisition sequence.

13. (previously presented) The memory of claim 10 containing executable instructions that cause the one or more processing devices to:

store a first more-preferred stored SID element, a second more-preferred stored SID element, and a plurality of less preferred SID elements in the roaming list; and

repeatedly attempt acquisition of the first more-preferred stored SID element, repeatedly attempt acquisition of the second more-preferred stored SID element and perform a single acquisition attempt of each of the at least one less-preferred stored SID element as part of performing the second more-preferred SID acquisition sequence.

14. (previously presented) A method for acquiring a more-preferred stored SID element in a wireless device, the method comprising:

storing a roaming list that includes a plurality of stored SID elements ranked according to an order of preference including at least one more-preferred stored SID element and at least one less-preferred stored SID element; and

performing a first more-preferred SID acquisition sequence and then a second more-preferred SID acquisition sequence that includes repeatedly attempting acquisition of the at least one more-preferred stored SID element using a same frequency during the second more-preferred SID acquisition sequence.

15. (original) The method of claim 14 including receiving broadcast SID information, wherein attempting acquisition is based on comparing the received broadcast SID information with one of the plurality of stored SID elements.

16. (original) The method of claim 14 further including:

camping on the at least one more-preferred stored SID element if acquisition of the at least one more-preferred stored SID element is available; and

camping on the at least one less-preferred stored SID element if acquisition of the at least one less-preferred stored SID element is available and if acquisition of the at least one more-preferred stored SID element is unavailable.

17. (original) The method of claim 15 further including performing the second more-preferred SID acquisition sequence if the more-preferred stored SID element is not acquired during the first more-preferred SID acquisition sequence.

18. (original) The method of claim 15 further including attempting acquisition of the at least one less-preferred stored SID element as part of performing the second more-preferred SID acquisition sequence.

19. (previously presented) A method for acquiring a more-preferred stored SID element in a wireless device, the method comprising:

storing a roaming list that includes a plurality of stored SID elements ranked according to an order of preference including at least one more-preferred stored SID element and a plurality of less-preferred stored SID elements;

performing a first more-preferred SID acquisition sequence and then a second more-preferred SID acquisition sequence that includes, during the second more-preferred SID acquisition sequence, repeatedly attempting acquisition of the at least one more-preferred stored SID element at a same frequency and a single acquisition attempt of each of the plurality of less-preferred stored SID elements not acquired during the first more-preferred SID acquisition sequence;

receiving broadcast SID information wherein attempting acquisition is performed by comparing the received broadcast SID information with one of the plurality of stored SID elements; and

repeatedly performing the second more-preferred SID acquisition sequence if acquisition of the plurality of stored SID elements in the roaming list is unavailable.

20. (original) The method of claim 19 further including:

camping on the at least one more-preferred stored SID element if acquisition of the at least one more-preferred stored SID element is available; and

camping on the at least one less-preferred stored SID element if acquisition of the at least one less-preferred stored SID element is available and if acquisition of the at least one more-preferred stored SID element is unavailable.

21. (previously presented) The circuit of claim 1 wherein the more-preferred stored SID element defines the home system.